

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Curtis Behmann on March 27, 2008.

2. The application has been amended as follows:

Claims 27-36 are canceled.

3. The following is an examiner's statement of reasons for allowance:

Regarding claim 10, the prior art of record fails to teach or fairly suggest a switch with egress port groups which are disabled using a plurality of flip flops coupled to the disable control memory, where each flip flop enables and propagates a ingress data grain to its egress port group and the toggling the particular ingress data grains based on the information stored in the data disable control memory.

Regarding claim 21, the prior art of record fails to teach or fairly suggest a switch for propagating transmission of data during a predefined interval of time to an egress port group, where the propagation is through a fanout tree. The propagation is disabled based on predefined data selection, where the disabling is done by overwriting by a constant value half the bits asserted by the ingress data disabled.

Regarding claim 22, the prior art of record fails to teach or fairly suggest a switch for propagating transmission of data during a predefined interval of time to an egress port group where the disabling of the propagation of ingress data is done by overwriting by a first constant value the ingress data derived from an enable signal through a first of at least one pair of sub-trees and by a second constant value the ingress data derived from an enable signal through a second of the at least one pair of sub-trees, such that the first and second constant values form a different complementary pair of constants for each pair of sub-trees.

Regarding claim 24, the prior art of record fails to teach or fairly suggest a switch comprising: a plurality of grain select blocks with a connection memory defining the egress data by a pre-selected ingress port and pre-selected timeslot; a multiplexer for selecting a ingress grain through a fanout based on the port and timeslot; and a timeslot counter and a lookahead timeslot counter to compensate for delay by running ahead of the main timeslot counter.

Regarding claim 25, the prior art of record fails to teach or fairly suggest a system for improving power efficiency of a memory switch comprising: a grain select block containing a fanout tree that propagates specific data to a subset of destinations; a data disable block that provides the connection from the source to the grain select block based on the fanout information; where the data is propagated over a pre-defined interval of time and the fanout information is generated as a signal propagating from a tail end of the fanout tree to a root end of the fanout tree to enable propagation of the data.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to BETTY LEE whose telephone number is (571)270-1412. The examiner can normally be reached on Monday-Thursday 9-5 EST and alternate Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou can be reached on (571) 272-3088. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Betty Lee
Examiner, Art Unit 2619

/Hassan Kizou/
Supervisory Patent Examiner, Art Unit 2619